

Рад у водећем часопису међународног значаја M20

1.	D. Prokin , M. Prokin, "Low Hardware Complexity Pipelined Rank Filter," <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , vol. 57, no. 6, pp. 446-450, 2010. IF=1,639	M21	8
2.	K. Kuk, D. Prokin , G. Dimić, "Interactive tasks as a supplement to educational material in the field of Programmable logic devices," <i>Electronics and Electrical Engineering</i> , no. 2(98), 2010. IF=0,659	M23	3
3.	D. Prokin , M. Prokin, "Real-time pipelined rank filter," <i>International Journal of Imaging Science and Engineering</i> , vol. 1, no. 1, pp. 21-26, Sep. 2007.	M23	3